SRAM Array Hierarchy Definition

- **Cell size is defined by height x width in wl/bl orientation correspondingly.**

- **Subarray is a matrix of cells enclosed by decoders and sense amplifier**

- **Macro is an independent operable memory array, consists of a matrix of subarray, power rail, r/w control, address, and I/O.**

- **Array is a matrix of macro with data path and control interface to compute unit**

I/O

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